

**In the Claims:**

**Please amend the claims as indicated below:**

1-5. (canceled)

6.(previously presented) A system comprising:

transistors adapted to operate as an SR flip-flop wherein each one of the transistors has an insulated gate and wherein the SR flip-flop is a SEUSSNor comprising:

a power node and four p type transistors named P2, P4, P6, and P8 wherein the sources of P2, P4, P6, and P8 are connected to the power node;

a ground node and eight n type transistors named N1, N2, N3, N4, N5, N6, N7, and N8 wherein the sources of N1, N2, N3, N4, N5, N6, N7, and N8 are connected to the ground node;

a p type transistor called P1 wherein the source of P1 is connected to the drain of P2;

a p type transistor called P3 wherein the source of P3 is connected to the drain of P4;

a p type transistor called P5 wherein the source of P5 is connected to the drain of P6;

a p type transistor called P7 wherein the source of P7 is connected to the drain of P8;

OK to ENTER  
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